

Assigned Papers for COP5622 - Fall 2007

1. M. E. Benitez and J. W. Davidson, "A Portable Global Optimizer and Linker," *Proceedings of the SIGPLAN '88 Symposium on Programming Language Design and Implementation*, pp. 329-338 (June 1988).
2. M. E. Benitez and J. W. Davidson, "The Advantages of Machine-Dependent Global Optimization," *Proceedings of the Conference on Programming Languages and Systems Architectures*, pp. 105-124 (March 1994).
3. M. Benitez and J. Davidson, "Target-Specific Global Code Improvement: Principles and Applications," Technical Report #42, Department of Computer Science, University of Virginia, Charlottesville, VA (1994).
4. F. Mueller and D. B. Whalley, "Avoiding Unconditional Jumps by Code Replication," *Proceedings of the SIGPLAN '92 Conference on Programming Language Design and Implementation*, pp. 322-330 (June 1992).
5. M. R. Boyd and D. B. Whalley, "Isolation and Analysis of Optimization Errors," *Proceedings of the SIGPLAN '93 Conference on Programming Language Design and Implementation*, pp. 26-35 (June 1993).
6. F. Mueller and D. B. Whalley, "Avoiding Conditional Branches by Code Replication," *Proceedings of the SIGPLAN '95 Conference on Programming Language Design and Implementation*, pp. 56-66 (June 1995).
7. G. R. Uh and D. B. Whalley, "Coalescing Conditional Branches into Efficient Indirect Jumps," *Proceedings of the International Static Analysis Symposium*, pp. 315-329 (September 1997).
8. M. Yang, G. Uh, and D. B. Whalley, "Improving Performance by Branch Reordering," *Proceedings of the SIGPLAN '98 Conference on Programming Language Design and Implementation*, pp. 130-141 (June 1998).
9. J. Mellor-Crummey, D. Whalley, and K. Kennedy, "Improving Memory Hierarchy Performance for Irregular Applications," *Proceedings of the ACM International Conference on Supercomputing*, pp. 425-433 (June 1999).
10. W. Krehling, D. Whalley, M. Bailey, X. Yuan, G. Uh, and R. van Engelen, "Branch Elimination via Multi-Variable Condition Merging," *Proceedings of Euro-Par 2003*, (August 2003).
11. R. Whaley and D. Whalley, "Tuning High Performance Kernels through Empirical Compilation," *International Conference on Parallel Processing*, pp. 89-98 (June 2005).
12. K. Pettis and R. Hansen, "Profile Guided Code Positioning," *Proceedings of the SIGPLAN '90 Conference on Programming Language Design and Implementation*, pp. 16-27 (June 1990).
13. A. Ayers, S. de Jong, J. Peyton, and R. Schooler, "Scalable Cross-Module Optimization," *Proceedings of the SIGPLAN '98 Conference on Programming Language Design and Implementation*, pp. 301-312 (June 1998).
14. T. Chilimbi, B. Davidson, and J. Larus, "Cache-Conscious Structure Definition," *Proceedings of the SIGPLAN '99 Symposium on Programming Language Design and Implementation*, pp. 13-24 (May 1999).
15. K. Cooper and N. McIntosh, "Enhanced Code Compression for Embedded RISC Processors," *ACM SIGPLAN Conference on Programming Language Design and Implementation*, pp. 139-149 (May 1999).
16. V. Bala, E. Duesterwald, and S. Banerjia, "Dynamo: A Transparent Dynamic Optimization System," *Proceedings of the SIGPLAN '00 Conference on Programming Language Design and Implementation*, pp. 1-12 (June 2000).

17. R. Zahir, J. Ross, D. Morris, and D. Hess, "OS and Compiler Considerations in the Design of the IA-64 Architecture," *Proceedings of the International Conference on Architectural Support for Programming Languages and Operating Systems*, pp. 212-221 (November 2000).
18. D. Jimenez, "Code Placement for Improving Dynamic Branch Prediction Accuracy," *Proceedings of the ACM SIGPLAN Conference on Programming Language Design & Implementation*, pp. 107-116 (June 2005).
19. J. Cavazos, G. Fursin, F. Agakov, E. Bonilla, M. O'Boyle, and O. Temam, "Rapidly Selecting Good Compiler Optimizations Using Performance Counters," *ACM/IEEE International Symposium on Code Generation and Optimization*, pp. 185-197 (March 2007).