

CDA3101 Compiler Organization II
Tentative Lecture Schedule - Spring 2015

Date	Topic	Sections from Textbook
1/8	Introduction	1.1-1.4, 1.6
1/13	Introduction, MIPS Assembly Introduction	1.7-1.8, 2.2-2.3
1/15	MIPS Assembly Introduction, Encoding Instructions	2.2, 2.5, 2.10
1/20	QtSpim, Arith/Logic, Data Transfers	A.9, 2.3, 2.6
1/22	Transfers of Control, Program Translation Process	2.7, 2.12
1/27	Logic Design Basics, Combinational Logic	B.2-B.3
1/29	Sequential Logic, Single Cycle Datapath	B.7-B.10, 4.1-4.3
2/3	Single Cycle Datapath	4.1-4.3
2/5	Single Cycle Control	4.3-4.4
2/10	Exam 1	
2/12	Review Exam 1	
2/17	Multicycle Datapath, Multicycle Control	
2/19	Pipelining Introduction, Pipeline Hazards	4.5
2/24	Pipeline Datapath	4.6
2/26	Pipeline Control	4.6-4.7
3/3	Pipeline Control	4.7-4.8
3/5	Exceptions, Multicycle Operations	4.9
3/10	no class (Spring Break)	
3/12	no class (Spring Break)	
3/17	More ILP	4.10
3/19	Exam 2	
3/24	Review Exam 2	
3/26	Memory Hierarchy Introduction	5.1-5.2
3/31	Caches	5.3
4/2	Caches	5.3-5.4
4/7	Caches	5.4
4/9	Caches	5.4, 5.9
4/14	Virtual Memory	5.7
4/16	Virtual Memory	5.7
4/21	I/O	5.11
4/23	I/O	
4/30	Final Exam 3:00pm-5:00pm	