

This course provides an introduction to computer organization. We will discuss the basic design of a processor, memory system and I/O system along with cost vs. performance tradeoffs for architectural enhancement strategies. We will also discuss the issues surrounding the intergration of multiple processors into a high-performance parallel computer system.

The following schedule is currently planned:

Classes	Chapter	Topic
2	1 and 2	Introduction and Performance Analysis
3	5	Datapath and Control
8	6	Pipelining
1	–	Midterm exam
7	7	Memory hierarchy
4	8	I/O system
3	9	Multiprocessors